## **Hall C HV Testing Project Status Report**

**Date: August 21, 2019** 

DSG Staff: Marc McMullen, Pete Bonneau, Pablo Campero

- 1. CAEN Software test stand load
  - 1.1. Chassis design and requisition completed.
- 2. New test chassis design.
  - 2.1.1. Multiplexer chassis design and requisition completed.
- 3. Full current test of the A7030TN modules
  - 3.1. 200 channels (> 5 modules) have been tested to full current.